

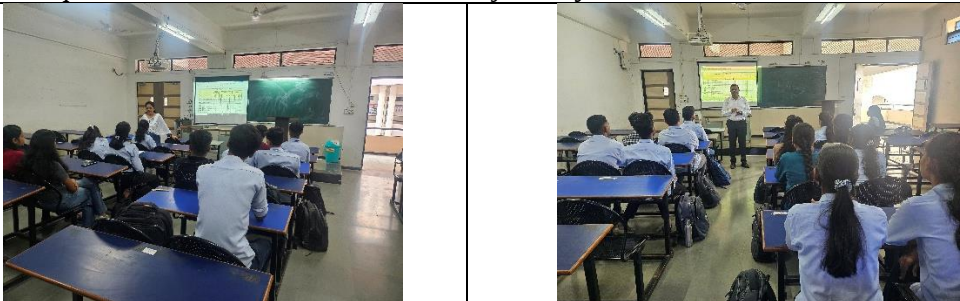


Department of Electronics and Telecommunication Engineering

Induction Program

Class:- SY BTECH

AY 2025-26.

Under Mission	M2: Committed to serve the needs of society through Industry- Institute Interaction
Objective	To orient second-year B.Tech students with the departmental structure, academic curriculum, outcome-based education (COs, POs), Bloom's Taxonomy, and support systems to help them succeed in their engineering journey.
Name of the Event	SY B.Tech Students Oriented Towards Outcome-Based Education
Date and Time of Event	01.07.2025 & 02.07.2005, 10.00 am Venue: C401, Third Floor, "C" building, Department of E&TC, KBTCOE Nashik
Audience	Second Year E&TC students.
Event Description	The Department of Electronics & Telecommunication Engineering organized an Induction Program for Second Year B.Tech students for the academic year 2025-26. The session aimed at welcoming students into the new academic phase and orient them with the departmental structure, faculty, and available resources. Key highlights of the program included an introduction to Outcome-Based Education (OBE), covering Course Outcomes (COs), Program Outcomes (POs), and Program Specific Outcomes (PSOs), along with a simplified overview of Bloom's Taxonomy. Students were briefed on the semester-wise curriculum, lab courses, assessment schemes, and support systems including mentoring and co-curricular opportunities. The interactive session concluded with a Q&A round to address student queries and motivate them for the journey ahead.
Photos of Event	

Ms. T.S.Deshmukh
Class Coordinator, SE

HOD

Department of Electronics and Telecommunication Engineering

Induction Program

Class - TE

AY 2025-26.

Under Mission	M2: Committed to serve the needs of society through Industry- Institute Interaction
Objective	To orient students toward the academic, professional, and for career opportunities through guidance on placements, internships, higher education, and entrepreneurship.
Name of the Event	Induction Program for Third Year Student AY 2025-26
Date and Time of Event	01.07.2025, 10.00 am Venue: C402, Third Floor, "C" building, Department of E&TC, KBT COE Nashik
Audience	TE students.
Event Description	The Induction Program for Final Year Engineering students is a structured initiative designed to orient and prepare students for the crucial final phase of their academic journey. This program serves as a bridge between academic learning and professional readiness, guiding students on academic expectations, placement preparation, and higher studies opportunities. Mr. Vishwesh Kathe, (training officer) special emphasis is placed on soft skills, resume building, and communication, essential for career advancement and institutional support available. Students also are introduced to final year project protocols, internship opportunities. The session includes expert talks by Mr. Dhiraj Matsagar, (SBI HR Manager) on financial awareness and Q&A forums, this program aims to motivate students, align their goals, and ensure a successful transition to the professional world.
Photos of Event	 Nashik, Maharashtra, India 2q87+2g9, D. K. Nagar, Nashik, Maharashtra 422005, India Lat 20.015061° Long 73.763912° 01/07/2025 11:08 AM GMT +05:30
	 Nashik, Maharashtra, India 2q87+2g9, D. K. Nagar, Nashik, Maharashtra 422005, India Lat 20.015061° Long 73.763903° 01/07/2025 12:24 PM GMT +05:30
	 Nashik, Maharashtra, India 2q87+2g9, D. K. Nagar, Nashik, Maharashtra 422005, India Lat 20.015072° Long 73.763862° 01/07/2025 11:38 AM GMT +05:30
	

Class Coordinator, TE

HOD

Department of Electronics and Telecommunication Engineering

Induction Program

Class - TE

AY 2025-26.

Under Mission	M2: Committed to serve the needs of society through Industry- Institute Interaction
Objective	To orient students toward the academic, professional, and for career opportunities through guidance on placements, internships, higher education, and entrepreneurship.
Name of the Event	Induction Program for Third Year Student AY 2025-26
Date and Time of Event	01.07.2025, 10.00 am Venue: C402, Third Floor, "C" building, Department of E&TC, KBT COE Nashik
Audience	TE students.
Event Description	The Induction Program for Final Year Engineering students is a structured initiative designed to orient and prepare students for the crucial final phase of their academic journey. This program serves as a bridge between academic learning and professional readiness, guiding students on academic expectations, placement preparation, and higher studies opportunities. Mr. Vishwesh Kathe, (training officer) special emphasis is placed on soft skills, resume building, and communication, essential for career advancement and institutional support available. Students also are introduced to final year project protocols, internship opportunities. The session includes expert talks by Mr. Dhiraj Matsagar, (SBI HR Manager) on financial awareness and Q&A forums, this program aims to motivate students, align their goals, and ensure a successful transition to the professional world.
Photos of Event	 Nashik, Maharashtra, India 2q87+2g9, D. K. Nagar, Nashik, Maharashtra 422005, India Lat 20.015061° Long 73.763912° 01/07/2025 11:08 AM GMT +05:30  Nashik, Maharashtra, India 2q87+2g9, D. K. Nagar, Nashik, Maharashtra 422005, India Lat 20.015061° Long 73.763903° 01/07/2025 12:24 PM GMT +05:30  Nashik, Maharashtra, India 2q87+2g9, D. K. Nagar, Nashik, Maharashtra 422005, India Lat 20.015072° Long 73.763862° 01/07/2025 11:38 AM GMT +05:30 

Class Coordinator, TE

HOD

Department of Electronics and Telecommunication Engineering

Induction Program

Class - TE

AY 2025-26.

Under Mission	M2: Committed to serve the needs of society through Industry- Institute Interaction
Objective	To orient students toward the academic, professional, and for career opportunities through guidance on placements, internships, higher education, and entrepreneurship.
Name of the Event	Induction Program for Third Year Student AY 2025-26
Date and Time of Event	01.07.2025, 10.00 am Venue: C402, Third Floor, "C" building, Department of E&TC, KBT COE Nashik
Audience	TE students.
Event Description	The Induction Program for Final Year Engineering students is a structured initiative designed to orient and prepare students for the crucial final phase of their academic journey. This program serves as a bridge between academic learning and professional readiness, guiding students on academic expectations, placement preparation, and higher studies opportunities. Mr. Vishwesh Kathe, (training officer) special emphasis is placed on soft skills, resume building, and communication, essential for career advancement and institutional support available. Students also are introduced to final year project protocols, internship opportunities. The session includes expert talks by Mr. Dhiraj Matsagar, (SBI HR Manager) on financial awareness and Q&A forums, this program aims to motivate students, align their goals, and ensure a successful transition to the professional world.
Photos of Event	   

Class Coordinator, TE

HOD



Department of Electronics and Telecommunication Engineering

Under Mission	Committed to serve the needs of society through innovative teaching – learning process, promoting industry- institute interaction to provide competent and cultured Electronics and Telecommunication Engineers.
Objective	1.To review and suggest improvements to the new TY B. Tech (SEM – I & II) & SY M. Tech (SEM – I & II) syllabus based on the National Education Policy (NEP) guidelines. 2. To ensure the syllabus content is aligned with the latest industry trends and meets the requirements of various engineering disciplines. 5. To discuss the inclusion of modern technologies and pedagogical approaches in the curriculum. 6. To open the floor for suggestions and discussions from the BOS members.
Name of the Event	Departmental Board Of Study Meeting.
Date and Time of Event	Day & Date: Monday,01/12/2025 Time: 10.00 AM to 03.00 PM Venue: C-408, Department of E & TC, 3rd Floor, Academic Building C, KBTCOE, Near Pumping Station, Gangapur Road, Nashik -13
Members present for meeting	Dr. S. L. Nalbalwar , Mr. Amol Deshmukh , Dr. Munir Sayyad , Ms. Komal Pise ,Dr. B.N. Rajole, Dr. B. N. Shinde, Dr. U.V. Patil, Mr. B.J. Pawar, Ms. S. A. Dhumane, Mr. A. R. Chaudhari, Ms. D.V. Patil, Ms. T.S. Deshmukh, Mr. V. R. Sonawane, Ms. S. M. Dharrao, Mr. N. A. Gade, Ms. Seema, Ms. Nimse, Ms. J. S. Patil, Ms. S. S. Gade, Mr. D. R. Handge, Ms. Shital Mogal.
Event Description	The Board of Studies (BoS) Meeting of the Electronics & Telecommunication Engineering Department of MVPS' KBTCOE Nashik was successfully conducted on 01/12/2025 at [C408-Hybrid Platform]. The meeting was chaired by Dr. B.N. Rajole, Chairman, Dr. M. H. Kori, (Retd. Technical Director, Alcatel-lucent & Technology Advisor to Validus Technologies, USA) as a subject Expert, Dr. S. L. Nalbalwar, (Professor & Head, Department of Electronics & Telecomm. Engg. BATU, Lonere Mangaon) as a subject Expert, Mr. Amol Deshmukh (Head R&D Rishabh Instruments Ltd. MIDC Satpur, Nashik Maharashtra India) as a industry person, Dr. Munir Sayyad (General Manager Reliance Corporate Park Thane Belapur Road Ghansoli, Ghansoli, Maharashtra, India) Industry Representative, Ms. Komal Pise (Director, Sri Sai precision Instruments, Satpur) with the presence of internal faculty members. The Board deliberated on important academic matters including: • Review and approval of the syllabus of TY B.Tech (SEM – I & II) & SY M. Tech (SEM – I & II) • Introduction of Electives. • Alignment of curriculum with industry and university guidelines. • Recommendations for academic regulations. The Board approved the proposed changes, which will be implemented from the [2026-2027 - SEM-I & II].



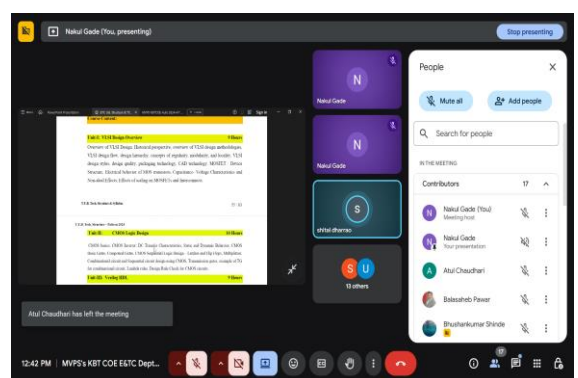
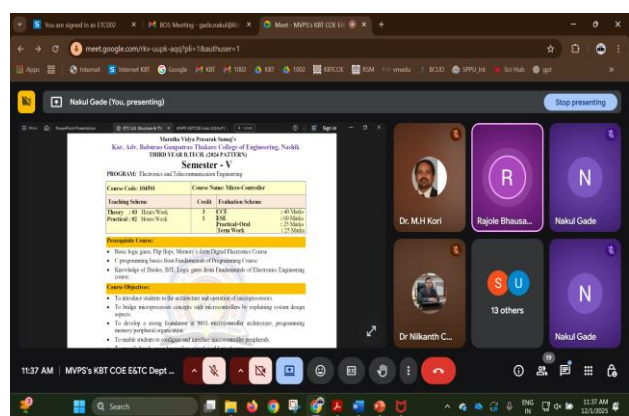
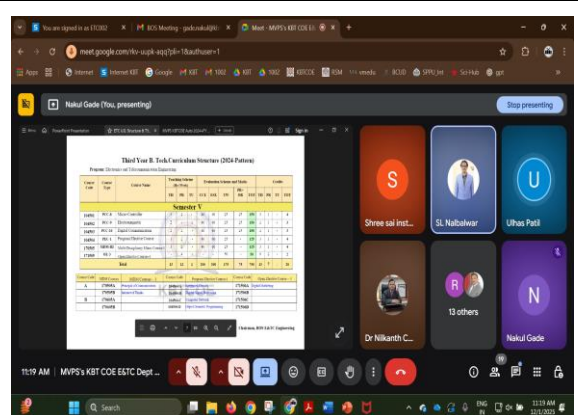
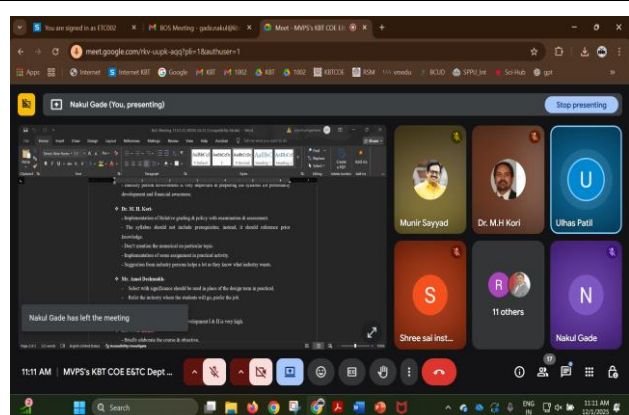
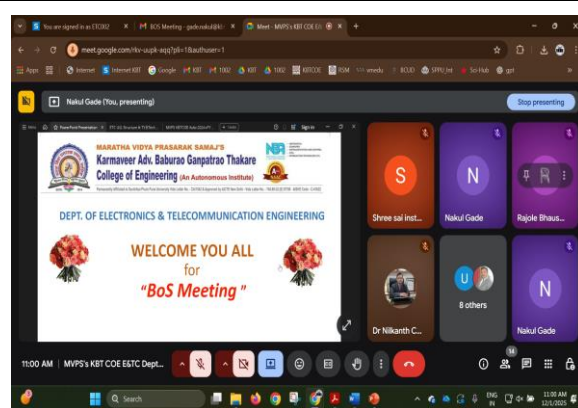
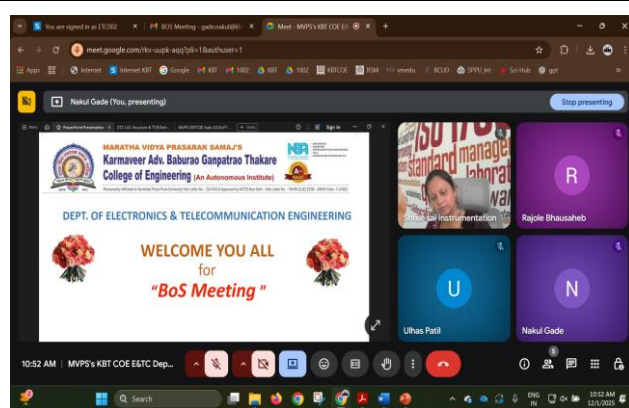
MARATHA VIDYA PRASARAK SAMAJ'S Karmaveer Adv. Baburao Ganpatrao Thakare College of Engineering (An Autonomous Institute)

Permanently Affiliated to Savitribai Phule Pune University Vide Letter No. : CA/1542 & Approved by AICTE New Delhi - Vide Letter No. : 740-89-32 (E) ET/98 AISHE Code - C-41622



Department of Electronics and Telecommunication Engineering

Quality
Photos



PO/PSO
Covered

NA



Department of Electronics and Telecommunication Engineering

Industrial Visit

Class:- SY BTECH

AY 2025-26.

Under Mission	Committed to serve the needs of society through innovative teaching – learning process, promoting industry- institute interaction to provide competent and cultured Electronics and Telecommunication engineers.	
Objective	To orient Second Year B.Tech students towards the department's vision, Outcome-Based Education framework, academic structure, and placement readiness by emphasizing curricular expectations and essential technical and soft skills.	
Name of the Event	Learning Beyond Classroom: Visit to Electronics Expo, Pune	
Date and Time of Event	10 TH Jan 2026, 1.30 pm Venue: Electronics Expo , Pune.	
Audience	Second Year E&TC students.	
Event Description	The Department of Electronics & Telecommunication Engineering, MVP's KBT College of Engineering, Nashik, organized an Industrial Visit to the Electronics Expo, Pune on 10th January 2026 for S. Y. B.Tech (E&TC) students. A total of 42 students, accompanied by two faculty members, participated in the visit. The visit was conducted between 1:30 pm and 4:00 pm. The Electronics Expo provided a valuable platform for students to interact with industry professionals and explore the latest innovations in electronics, communication systems, embedded systems, IoT, automation, and VLSI technologies. Various companies and start-ups showcased real-world applications, industrial products, and emerging technologies relevant to the E&TC curriculum. Students gained first-hand exposure to modern electronic components, communication equipment, PCB design, microcontroller-based systems, and industrial automation solutions. They actively interacted with exhibitors and clarified their doubts regarding industry requirements, career opportunities, and technological trends. The visit successfully bridged the gap between theoretical learning and practical industrial exposure, enhancing students' understanding of how classroom concepts are applied in real engineering environments.	
Photos of Event	 	

Ms. T.S.Deshmukh
Class Coordinator, SY

Dr. B. N. Rajole
HOD



Department of Electronics and Telecommunication Engineering

Parents Teacher Meet

AY 2025-26.

Under Mission	Committed to serve the needs of society through innovative teaching – learning process, promoting industry- institute interaction to provide competent and cultured Electronics and Telecommunication engineers.
Objective	The objective of the PTA Meet is to strengthen communication between parents and the department by sharing academic plans, student performance, and developmental activities. It also aims to address parents' queries and seek feedback for the continuous improvement of students' academic and professional growth.
Name of the Event	Parents Teachers Meet
Date and Time of Event	13.08.2025, 10.00 am -12.00 am Venue: C403, Third Floor, "C" building, Department of E&TC, KBTCOE Nashik
Audience	Parents
Event Description	The Department of Electronics & Telecommunication Engineering organized a Parents-Teacher Meet on 13 August 2025. The agenda of the meeting was to welcome all attendees, present the mission and vision of the department, share the academic plans for the year 2025–26, review the last semester's performance, discuss placement opportunities, and convey the Continuous Comprehensive Evaluation (CCE) marks to the parents of SY B.Tech students. The session commenced with a welcome address by Dr. U. V. Patil, who also discussed all agenda points in detail. It was informed that there had been a noticeable improvement in students' semester result pointers. The S.Y. B.Tech Autonomy Exam process and the results of the 1st CCE were informed to parents. Information was also shared regarding the various seminars and field visits arranged for students. Mr. Vishwas Kathe, Training & Placement representative, spoke about the placement criteria, salary packages offered, and the essential skills required to secure a job in the industry. The importance of student chapters was highlighted by Dr. B. N. Shinde, emphasizing their role in student growth and exposure. An introduction of Mrs. Rupa Shinde and her role in departmental activities was given by Dr. B.N.Rajole. It was also mentioned that faculty members stay updated by actively participating in workshops and paper presentations. The meeting concluded with a vote of thanks by Ms. T. S. Deshmukh, expressing gratitude to all parents, faculty, and students for their active participation. A question-answer session followed, where the queries of parents regarding academics, placements, and student development were addressed satisfactorily by the faculty members.
Photos of Event	 



HOD



Department of Electronics and Telecommunication Engineering

Parents Teacher Meet

AY 2025-26.

Under Mission	Committed to serve the needs of society through innovative teaching – learning process, promoting industry- institute interaction to provide competent and cultured Electronics and Telecommunication engineers.
Objective	The objective of the PTA Meet is to strengthen communication between parents and the department by sharing academic plans, student performance, and developmental activities. It also aims to address parents' queries and seek feedback for the continuous improvement of students' academic and professional growth.
Name of the Event	Parents Teachers Meet
Date and Time of Event	13.08.2025, 10.00 am -12.00 am Venue: C403, Third Floor, "C" building, Department of E&TC, KBTCOE Nashik
Audience	Parents
Event Description	The Department of Electronics & Telecommunication Engineering organized a Parents-Teacher Meet on 13 August 2025. The agenda of the meeting was to welcome all attendees, present the mission and vision of the department, share the academic plans for the year 2025–26, review the last semester's performance, discuss placement opportunities, and convey the Continuous Comprehensive Evaluation (CCE) marks to the parents of SY B.Tech students. The session commenced with a welcome address by Dr. U. V. Patil, who also discussed all agenda points in detail. It was informed that there had been a noticeable improvement in students' semester result pointers. The S.Y. B.Tech Autonomy Exam process and the results of the 1st CCE were informed to parents. Information was also shared regarding the various seminars and field visits arranged for students. Mr. Vishwas Kathe, Training & Placement representative, spoke about the placement criteria, salary packages offered, and the essential skills required to secure a job in the industry. The importance of student chapters was highlighted by Dr. B. N. Shinde, emphasizing their role in student growth and exposure. An introduction of Mrs. Rupa Shinde and her role in departmental activities was given by Dr. B.N.Rajole. It was also mentioned that faculty members stay updated by actively participating in workshops and paper presentations. The meeting concluded with a vote of thanks by Ms. T. S. Deshmukh, expressing gratitude to all parents, faculty, and students for their active participation. A question-answer session followed, where the queries of parents regarding academics, placements, and student development were addressed satisfactorily by the faculty members.
Photos of Event	 



HOD

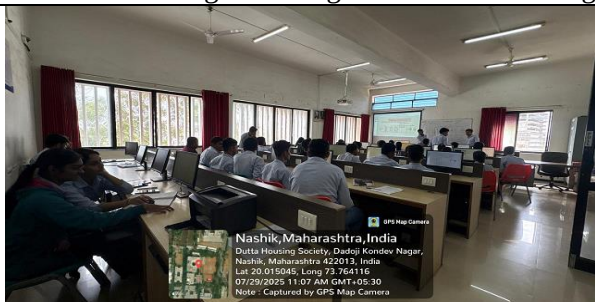


Department of Electronics & Telecommunication Engineering

Name of the Event	Industrial Visit at Radio Vishwas 90.8 Total number of students - 55		
Under Mission	Under Mission M2 : Committed to serve the needs of society through promoting industry- institute interaction to provide competent and cultured Electronics and Telecommunication Engineers.		
Objective	To Study The Basic Overview Of Digital Communication And Its Wide Applications For Broadcasting. Students Can See How Theoretical Concepts Like, Multiplexing, Error Correction, And Signal Processing Are Implemented In Real-World Systems. Software (Protocols, Algorithms) Involved In Digital Communication Networks.		
Date and Time	28/08/2025, 1.00 to 3.30		
Event Description	During the visit, we were introduced to complete process of radio broadcasting the key highlights include: a. Recording Microphone: 3 SM-58 dynamic mic's are used. dynamic are used cause they record voice which is nearer to them. This microphone record voice of frequency 20Hz to 20KHz. Soundproofing: Sound has property to reflect on solid surface and reverting back known as echo. To prevent Echo walls in recording room are made of acoustic panels also known as foam, which has ability to absorb sound. Students observed the entire signal chain—from voice capture using SM-58 dynamic microphones and echo-controlled recording rooms, through analog-to-digital conversion, audio processing (mixing, buffering, noise removal with Adobe Audition), program scheduling, and digital-to-analog transmission via the transmitter—demonstrating real-time application of modulation, signal processing, ADC/DAC, buffering, and redundancy. Total 55 students were present for visit, students observed and interact with expertise.		
Quality Photos	 		
	 		
PO Cover	PO1, PO3, PO5, PO12		



Department of Electronics & Telecommunication Engineering

Name Of The Event	Workshop on Proteus Software. (Conducted By TE students) (Harsh Patil , Siddharth Kedare , Vedant Jagtap , Nirmal Gawali)
Date And Time Of Event	Monday, Tuesday, 28-29-31/07/2025, 10.00AM to 12.00 PM
Mission And Objective	Mission: Committed to serve the needs of society through innovative teaching – learning process. Objective: 1.The prime objective of this workshop was to enhance simulation skills of students using one of the most popular simulation software such as Proteus. 2.Participants can learn how to use Proteus to design, simulate, and analyze electronic circuits.
Event Description	The Department of Electronics and Communication Engineering hosted a one-day workshop focused on Proteus Software. A total of 55 students engaged in this workshop, and we received affirmative feedback from each participant. The entire one-day workshop session was delivered by TE Students). The main objectives of the Workshop were to develop knowledge and understanding about how simulation software is used for analyzing the analog and digital circuits and to acquaint students with the Installation Process & steps for creating Circuit Using software. During the first session the introduction on Pspice, how it works and variety of simulation software available for circuit simulation like tinkercad, Multisim, Proteus, circuit maker etc. was discussed. The students also attended hands the use of a computer simulation program allows to to various stimuli (such as input signals and power supply variations) explained. The hands-on session. During the second session how, he easily subjects the circuit profiles & Selection of different analysis methodology. The session was good enough for the students to get used to Proteus.
	 
	 
PO	PO1, PO2, PO5



Department of Electronics & Telecommunication Engineering

Name of the Event	Visit to Shivananda Electronics, Devlali Camp, Nashik Total no. of students- S.Y B.Tech - 68 Staff: 1] Dr. S. M. Jagtap 2] Mr. A.R. Chuadhari 3] Ms. N.A. Gade 4] Ms. J. S. Patil		
Date and Time of Event	26 th September 2025 [1:00 pm to 4:30 pm]		
Event Description	Under Mission M2: Committed to serve the needs of society through Industry- Institute Interaction today, 26 th September 2025, SY. Btech students of E&TC Engg visited SIVANANDA ELECTRONICS along with Prof. Dr. S. M. Jagtap, Mr. A.R. Chuadhari, Ms. N.A. Gade, Ms. J. S. Patil with objectives 1. To understand electronic manufacturing process of security products. 2. To understand various test equipments used for manufacturing process.		
Quality Photos			
			
PO Covered	PO1,PO2,PO6,PO12		



MARATHA VIDYA PRASARAK SAMAJ'S

Karmaveer Adv. Baburao Ganpatrao Thakare
College of Engineering (An Autonomous Institute)



Permanently Affiliated to Savitribai Phule Pune University Vide Letter No. : CA/1542 & Approved by AICTE New Delhi - Vide Letter No. : 740-89-32 (E) ET/98 AISHE Code - C-41622

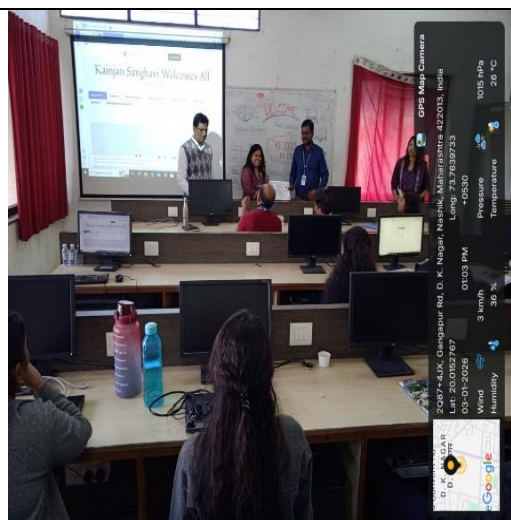
Department of Electronics and Telecommunication Engineering

Faculty Development Program

AY 2025-26.

Under Mission	Committed to serve the needs of society through innovative teaching – learning process, promoting industry- institute interaction to provide competent and cultured Electronics and Telecommunication engineers.
Objective	To equip with practical knowledge and skills to effectively integrate AI-based tools into teaching, learning, assessment and research for enhanced academic quality and productivity
Name of the Event	Faculty Development Program
Date and Time of Event	1 Jan 2026 to 3 Jan 2026 , 10.00 am -1.00 pm Venue: C410A, Third Floor, “C” building, Department of E&TC, KBTCOE, Nashik
Audience	Faculty
No. of Participants	60
Event Description	The Department of Electronics & Telecommunication Engineering organized a Faculty Development Program from 1 Jan 2026 to 3 Jan 2026. The agenda of the FDP was to improving teaching effectiveness through AI-assisted presentation design and automation of academic through expert sessions, demonstrations and interactive discussions, the program encourages responsible and ethical adoption of AI in education. The session commenced with a welcome address by Mr. N. A. Gade, who also discussed all agenda points in detail. On the 1st day Mr. P. N. Achaliya, Asst.Prof, Computer Engg, SNJB Chandwad, Nashik, spoke about the Gamma and Research Writing. On 2nd day Dr. M. R. Sanghavi, Prof. & Vice Principal SNJB Chandwad, Nashik, delivered knowledge about Effective Use of Chatgpt and NotebookLM. On 3rd day Dr. K.M. Sanghavi, Prof. & Head of Dept Computer Engg, SNJB Chandwad, Nashik, spoke about the School Magic, Zotero and Excel Automation. A question–answer session followed, where the queries of faculty regarding academics and AI-tools were addressed satisfactorily by the speakers. The FDP concluded with a vote of thanks by Ms. T. S. Deshmukh, expressing gratitude to all FDP speakers, faculty participant and Co-ordinators for their active Support and coordination.

Photos of Event



Event Coordinator

HOD

Industrial Visit

- Electronics Expo, Pune (10/01/2026)



Department of Electronics and Telecommunication Engineering organized an Electronics Expo, Pune on 10th January 2026. During this visit, The Electronics Expo provided a valuable platform for students to interact with industry professionals and explore the latest innovations in electronics, communication systems, embedded systems, IoT, automation, and VLSI technologies. The students of SY-BTech (E&TC) took the benefit of this visit.

Faculty Development Programme



The Department of Electronics & Telecommunication Engineering organized a Faculty Development Program from 1 Jan 2026 to 3 Jan 2026. The agenda of the FDP was to improving teaching effectiveness through AI-assisted presentation design and automation of academic through expert sessions, demonstrations and interactive discussions, the program encourages responsible and ethical adoption of AI in education.

SY B.Tech Students Oriented Towards Outcome-Based Education



The Department of Electronics & Telecommunication Engineering organized an Induction Program for Second Year B.Tech students (Semester VI) for the Academic Year 2025–26. The program began with an introduction to the Vision and Mission of the College and the Department, emphasizing the academic goals and professional values expected from students. An overview of Outcome-Based Education (OBE) was presented, explaining Course Outcomes (COs), Program Outcomes (POs), and Program Specific Outcomes (PSOs) to help students understand the expected learning outcomes and competencies.

Faculty Visit to IIT Bombay



Ms. S. A. Dhumane, Ms. T. S. Deshmukh, Mr. V. R. Sonawane attended one day (30/01/2026) **University Affiliate Program (UAP)**—a key initiative by SemiX IIT Bombay to build a robust and industry-aligned Semiconductor Education and Training at IIT Bombay.



Department of Electronics and Telecommunication Engineering

Inauguration Of IETE Chapter & Seminar On Opportunities In Space Technology

AY 2025-26.

Under Mission	Committed to serve the needs of society through innovative teaching – learning process, promoting industry- institute interaction to provide competent and cultured Electronics and Telecommunication engineers.	
Objective	- To establish the IETE Student Chapter in the college. - To inform students about various opportunities in space and communication technologies.	
Name of the Event	INAGURATION OF STUDENTS IETE CHAPTER	
Date and Time of Event	07-10-2025, 10.00am – 12.00 pm Venue: Seminar Hall “C” building, Department of E&TC, KBTCOE Nashik	
Audience	IETE members	
Event Description	The Department of Electronics and Telecommunication Engineering organized the Inauguration Ceremony of the IETE Student Chapter at MVP’s Karmaveer Adv. Baburao Ganpatrao Thakare College of Engineering, Nashik. The event marked the beginning of a dynamic platform for students to engage in professional development, technical activities, and innovative learning under the Institution of Electronics and Telecommunication Engineers (IETE). A total of 150 students enrolled as IETE members, signifying a strong start for future technical initiatives and collaborations. The Chief Guest, Dr. Apurva Jakhadi, a renowned Space Educator, delivered an enlightening seminar on “Opportunities in Space Technology”, inspiring students to explore the vast potential and career avenues in the field of space science and technology.	
Photos of Event		



Department of Electronics and Telecommunication Engineering

Induction Program

Class:- BE

AY 2025-26.

Under Mission	M2: Committed to serve the needs of society through Industry- Institute Interaction
Objective	To orient students toward the academic, professional, and for career opportunities through guidance on placements, internships, higher education, and entrepreneurship.
Name of the Event	Induction Program for Final Year Student AY 2025-26
Date and Time of Event	01.07.2025, 10.00 am Venue: C405, Third Floor, "C" building, Department of E&TC, KBTCOE Nashik
Audience	BE students.
Event Description	The Induction Program for Final Year Engineering students is a structured initiative designed to orient and prepare students for the crucial final phase of their academic journey. This program serves as a bridge between academic learning and professional readiness, guiding students on academic expectations, placement preparation, and higher studies opportunities. Ms. Vinita Kare (Asst. TPO) & Mr. Vishwesh Kathe, (training officer) special emphasis is placed on soft skills, resume building, and communication, essential for career advancement and institutional support available. Students also are introduced to final year project protocols, internship opportunities. The session includes expert talks by Mr. Dhiraj Matsagar, (SBI HR Manager) on financial awareness and Q&A forums, this program aims to motivate students, align their goals, and ensure a successful transition to the professional world.
Photos of Event	   



Mr. A. R. Chaudhari
Class Coordinator, BE

HOD