



MARATHA VIDYA PRASARAK SAMAJ'S

**Karmaveer Adv. Baburao Ganpatrao Thakare
College of Engineering (An Autonomous Institute)**



Permanently Affiliated to Savitribai Phule Pune University Vide Letter No. : CA/1542 & Approved by AICTE New Delhi - Vide Letter No. : 740-89-32 (E) ET/98 AISHE Code - C-41622

DEPARTMENT OF ELECTRICAL ENGINEERING



NEWS LETTER

2025-26

VOL - I ISSUE - 1

About Department:

We are delighted to welcome you to the Department of **Electrical Engineering** at Maratha Vidya Prasarak Samaj's Karmaveer Adv. Baburao Ganapatrao Thakare College of Engineering, Nashik. Established in 2024, our department is dedicated to delivering high-quality technical education and fostering innovation, problem-solving skills, and professional excellence.

Our experienced faculty members, with strong academic and research backgrounds, are committed to guiding and mentoring students. We take pride in our state-of-the-art laboratories, modern infrastructure, and industry-relevant curriculum, designed to equip students with the knowledge and skills required for a successful career in electrical engineering.

At our department, we believe in holistic development, ensuring that students not only excel academically but also grow into competent professionals ready to tackle real-world challenges. We are excited to be part of your educational journey and look forward to supporting you in achieving your goals.

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VISION, MISSION AND OBJECTIVES OF THE DEPARTMENT

VISION

- To be a recognized Electrical Engineering Department developing competent and innovative professionals for sustainable societal and industrial development.

MISSION

- M1: To develop strong engineering knowledge, practical competencies, and problem-solving abilities in electrical engineering.
- M2: To foster research, innovation, entrepreneurship, and industry engagement for technological advancement.
- M3: To cultivate values and multidisciplinary learning for sustainable societal development.

PROGRAM EDUCATIONAL OBJECTIVES

- PEO-1: Establish successful careers in industry, public sector, research organizations, or entrepreneurship.
- PEO-2: Develop sustainable engineering solutions for industrial and societal challenges.
- PEO-3: Demonstrate professional ethics, effective communication, teamwork, leadership, and lifelong learning.

EDITORIAL TEAM

Ms. Ashwini V. Tidke
EDITOR-IN-CHIEF

Mr. Rushikesh Mahajan
EDITOR

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S.Y B-TECH PARENTS-TEACHER MEET, ACADEMIC YEAR 2025-2026



PARENTS LISTENING THE ADDRESS GIVEN BY H.O.D DR. Y. P. PATIL SIR

As part of curriculum the Electrical Engineering department has conducted parent-teacher meet on 26.07.2025. Head of department Dr. Y. P. Patil shared the activities and efforts taken by the department for the empowerment of the students and all stakeholders.

As part of the Parent-Teacher Meet organized for Second Year B.Tech (Electrical Engineering) students, Dr. Y. P. Patil, Head of the Department, delivered a detailed and insightful address to the parents. The session aimed to strengthen the collaboration between faculty and families to ensure the academic and personal development of students.

Dr. Patil began by providing a comprehensive overview of the curriculum and academic structure of the Second Year B.Tech program. He discussed the subject framework, credit system, and assessment methodology. He also explained the concept of autonomy, highlighting how it enables the department to regularly update the curriculum in alignment with current industry trends and technological advancements, while still adhering to regulatory norms prescribed by the affiliating university and AICTE.

A key point of emphasis was the importance of regular attendance, where Dr. Patil underscored that consistent classroom presence not only ensures better understanding of academic concepts but also instills discipline and time management essential attributes for professional success.

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DEPARTMENT OF ELECTRICAL ENGINEERING

FACULTY PARTICIPATED IN ONE-WEEK AICTE OFFLINE FDP



PARTICIPATION OF MRS.KOMAL.S.ZOPE IN THE PROGRAM ON “EFFECTIVE APPLICATION OF OPTIMIZATION TECHNIQUES FOR ELECTRIC VEHICLE”

Mrs.Komal.S.Zope, Faculty of Electrical Engineering, successfully completed a one-week offline Faculty Development Program (FDP) on “Effective application of optimization techniques for Electric Vehicle” from 30th June 2025 to 5th July 2025. The FDP was organized by ATAL to enhance faculty members' understanding of advanced optimization techniques and their application in electric vehicles, aiming to improve teaching, research, and innovation in sustainable mobility and energy-efficient technologies.

This program helped faculty develop in the evolving landscape of electric mobility, optimization techniques play a pivotal role in enhancing the performance, efficiency, and sustainability of Electric Vehicles (EVs). This session aims to provide insights into the practical application of advanced optimization methods in key areas such as battery management, energy consumption, route planning, powertrain control, and thermal management. This event is ideal for engineering students, researchers, and EV professionals seeking to gain a competitive edge in the electric mobility sector through the intelligent use of optimization.

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S.Y B-TECH PARENTS- TEACHER MEET-2, ACADEMIC YEAR 2025-2026, SEM-I

As part of curriculum the Electrical Engineering department has conducted parent-teacher meet on 27.09.2025. Head of department Dr. Y. P. Patil shared the activities and efforts taken by the department for the empowerment of the students and all stakeholders.

As part of the Parent-Teacher Meet organized for Second Year B.Tech (Electrical Engineering) students, Dr. Y. P. Patil, Head of the Department, delivered a detailed and insightful address to the parents. The session aimed to strengthen the collaboration between faculty and families to ensure the academic and personal development of students.

Dr. Patil began by providing a comprehensive overview of the curriculum and academic structure of the Second Year B.Tech program.

A key point of emphasis was the importance of regular attendance, where Dr. Patil underscored that consistent classroom presence not only ensures better understanding of academic concepts but also instills discipline and time management — essential attributes for professional success. Following the address, an interactive feedback session was held, where parents shared their thoughts, offered suggestions, and appreciated the department's efforts in maintaining transparency and quality education. The feedback received was encouraging and constructive, reflecting the parents' keen interest in the progress of their wards.



**PARENTS LISTENING THE ADDRESS GIVEN BY
H.O.D DR. Y. P. PATIL SIR**

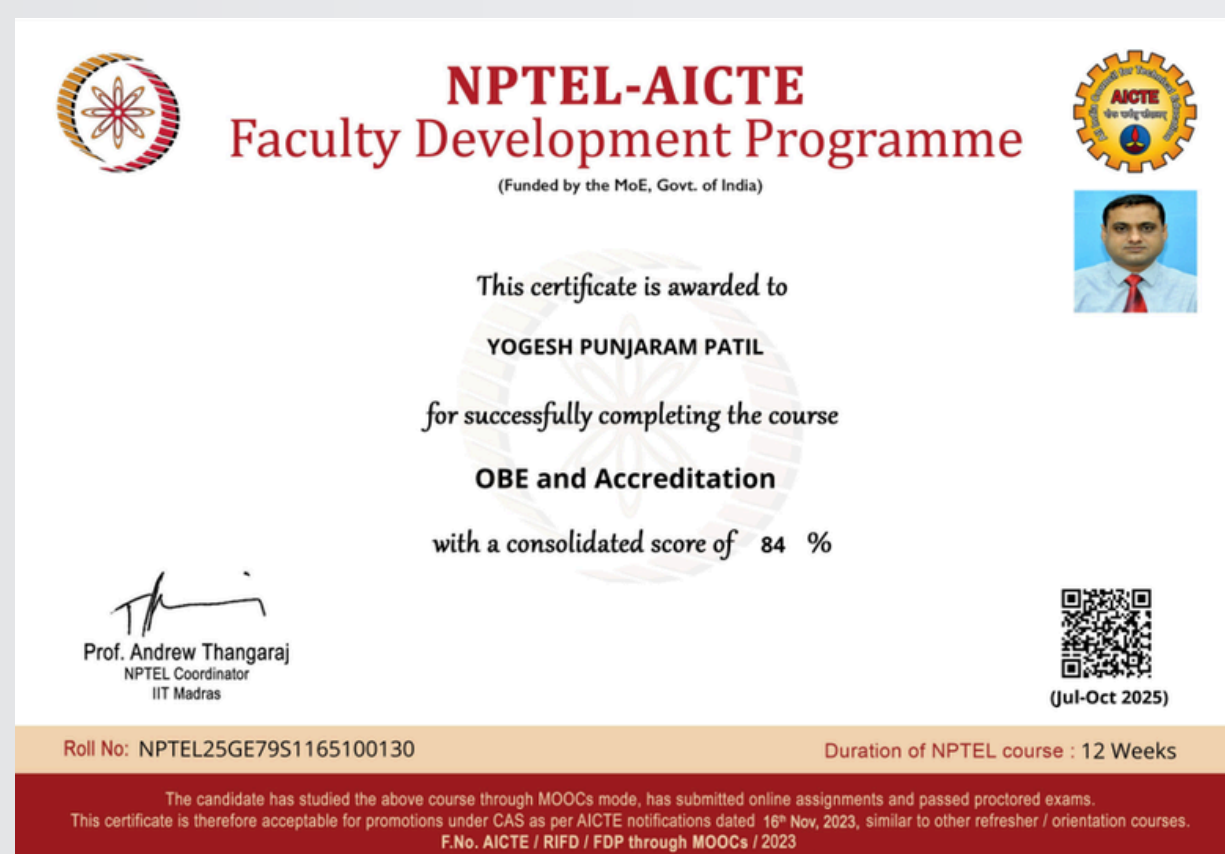


PARENTS SHARING THEIR VALUABLE VIEWS.

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12 WEEK ONLINE NPTEL/AICTE FDP ON “OBE AND ACCREDITATION”



PARTICIPATION OF DR. Y.P. PATIL IN THE PROGRAM ON “OBE AND ACCREDITATION”

Dr. Y. P. Patil, Faculty of Electrical Engineering, successfully completed a 12-week online Faculty Development Program (FDP) on “Outcome-Based Education and Accreditation” conducted through NPTEL–AICTE from July 2025 to October 2025.

This FDP was designed to enhance faculty understanding of the principles and implementation strategies of Outcome-Based Education (OBE) and the National Board of Accreditation (NBA) framework. The course provided comprehensive insights into curriculum design, course outcome formulation, CO–PO mapping, attainment calculation, and continuous improvement processes.

Through this program, participants gained valuable exposure to the Washington Accord Graduate Attributes (WAs), Program Outcomes (POs), and Assessment–Evaluation mechanisms essential for quality assurance in engineering education. The FDP also emphasized pedagogical innovation, effective teaching–learning methodologies, and the integration of modern tools for data-driven decision-making in academics.

By completing this FDP, Dr. Y.P. Patil strengthened his understanding of the OBE framework and accreditation processes, which will contribute to enhancing curriculum delivery, course outcome mapping, and overall academic quality within the department.

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THIRD BOARD OF STUDIES (BOS-3) MEETING – ELECTRICAL ENGINEERING



The Department of Electrical Engineering conducted the Third Board of Studies (BOS-3) meeting with participation from academic experts, industry representatives, alumni members, and faculty. The meeting focused on curriculum development for Third Year and Final Year B.Tech programs, statutory requirements under NEP 2020, exit policy implementation, laboratory strengthening, and industry-oriented reforms.

Major Highlights

- Approval of revised TY (Semester V & VI) curriculum and review of Final Year draft structure.
- Introduction of 'Introduction to Cyber Security' as a mandatory 4-credit course.
- Approval of FY and SY Exit Policy with Certificate and Diploma provisions.
- Strengthening of laboratories with mandatory hardware-based experiments.
- Recommendation of Honours tracks in Electric Vehicles, Smart Grid, and Energy Management.
- Standardized Internship and Project framework approved

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FACULTY PARTICIPATION DR. Y. P. PATIL SERVES AS SESSION CHAIR AT INTERNATIONAL CONFERENCE ICEC2NT 2025



DR. Y.P. PATIL RECEIVED A CERTIFICATE OF APPRECIATION FOR HIS
ROLE AS SESSION CHAIR, FORMALLY RECOGNIZING HIS
CONTRIBUTION TO THE SUCCESSFUL CONDUCT OF THE
CONFERENCE.

Overview:

The Department of Electrical Engineering is proud to announce that Dr. Y.P. Patil, Head of Department, successfully served as a Session Chair in the 2025 International Conference on Electronics and Computing, Communication Networking, Automation Technologies (ICEC2NT), held on 3rd – 4th September 2025 at D.Y. Patil College of Engineering, Akurdi, Pune. The conference was organized by D.Y. Patil College of Engineering, Pune, and brought together experts, researchers, and academicians from across India and abroad.

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The conference included:

- Technical paper presentations
- Keynote speeches by eminent researchers
- Panel discussions on latest advancements in electrical and electronics engineering
- Sessions on industry-academia collaboration and emerging technologies

Role and Responsibilities as Session Chair:

As Session Chair, Dr. Y.P. Patil was responsible for:

- Moderating technical sessions and ensuring smooth conduction of presentations.
- Facilitating discussions and Q&A sessions among presenters and participants.
- Encouraging active engagement between researchers, academicians, and industry representatives.
- Maintaining time schedules and coordinating with conference organizers to ensure session efficiency.

Significance of the Role:

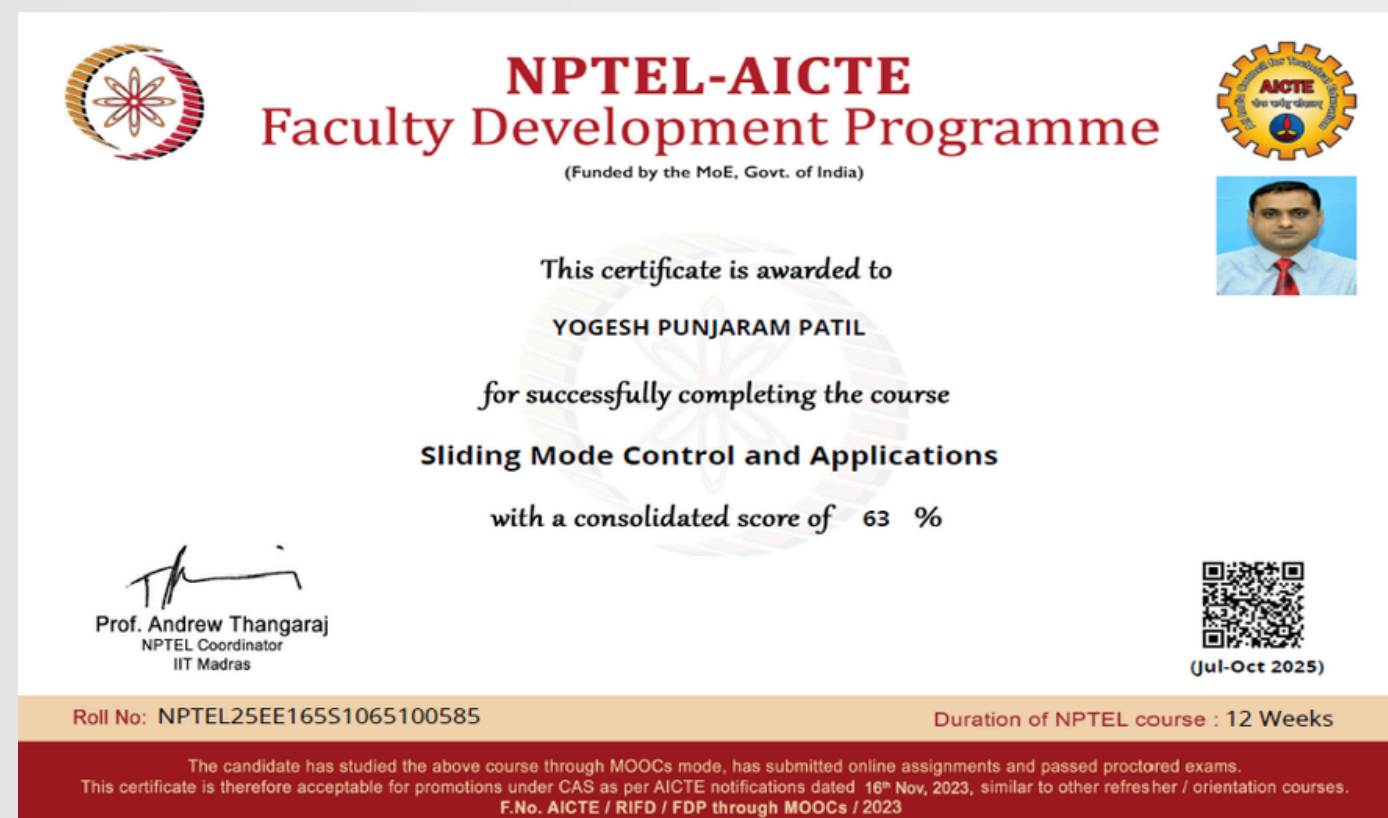
Serving as Session Chair at an international conference highlight:

- Recognition of Dr. Y.P.Patil's expertise in Electrical Engineering and related emerging technologies.
- Active contribution of KBTCOE faculty in national and international academic forums.
- Opportunity for the department to enhance its visibility in research and academic collaborations.
- Encouragement for students and faculty to participate in high-quality research conferences and stay updated with global technological trends.

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12-WEEK NPTEL-AICTE FACULTY DEVELOPMENT PROGRAM ATTENDED BY DR.Y.P. PATIL



PARTICIPATION CERTIFICATE OF DR. Y.P. PATIL IN THE PROGRAM ON “SLIDING MODE CONTROL AND APPLICATIONS”

Dr. Y. P. Patil, Head of the Department of Electrical Engineering, MVPS's KBT College of Engineering, Nashik, successfully completed a 12-week online Faculty Development Program (FDP) titled “Sliding Mode Control and Applications”, offered by NPTEL-AICTE and conducted by Prof. Shyam Kamal, IIT (BHU) Varanasi.

This advanced course focused on modern nonlinear control strategies for systems operating under heavy uncertainties. It provided in-depth exposure to finite-time and fixed-time stability, sliding mode control (SMC) design, higher-order SMC, adaptive SMC, and observer-based control. The curriculum included practical demonstrations through simulations, highlighting robust performance under disturbances and noisy measurements.

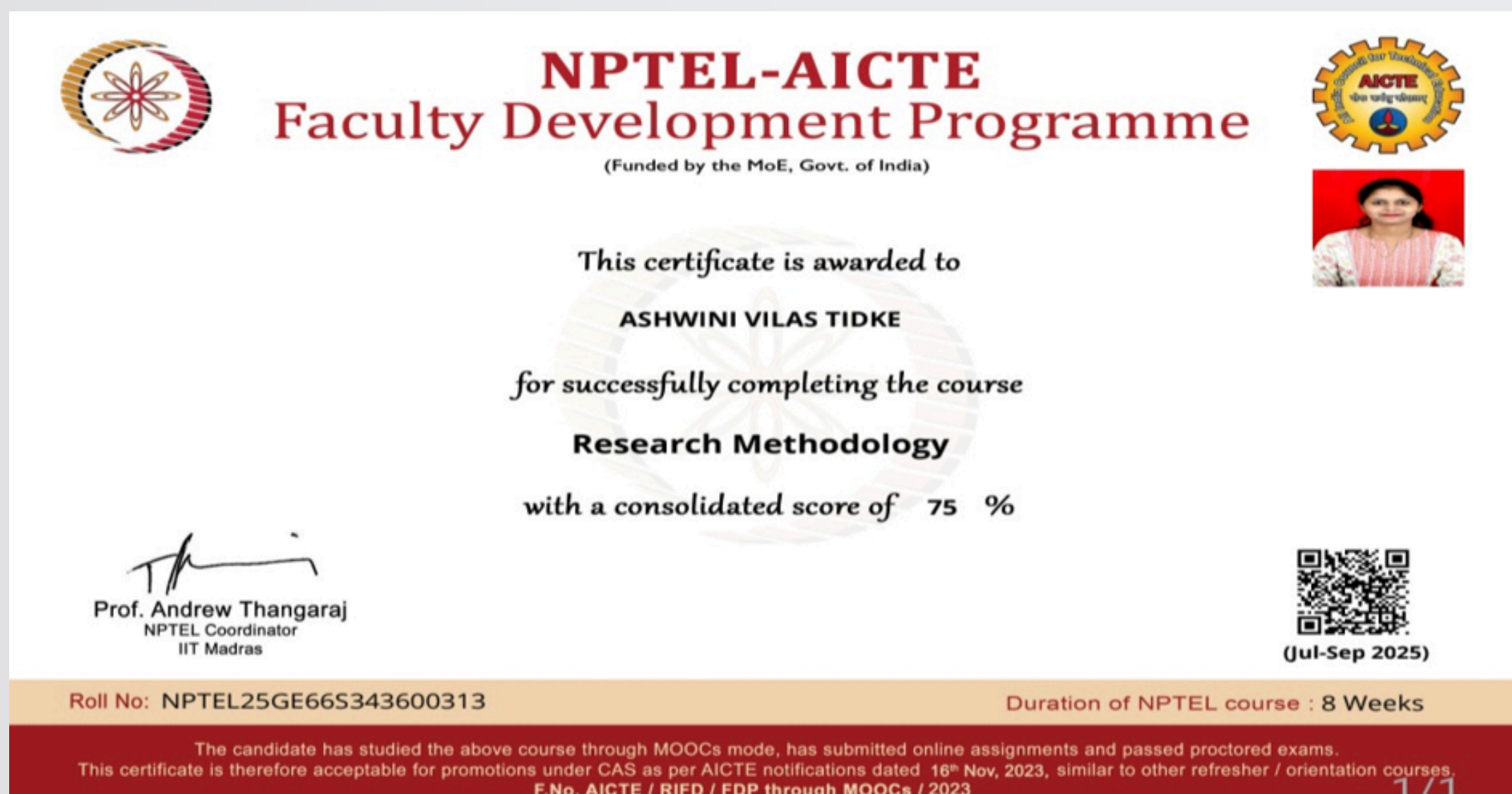
The course extensively covered multivariable SMC, integral sliding modes, feedback linearization, super-twisting algorithms, discrete-time SMC, and optimization-based SMC design, enabling participants to apply these techniques to complex multi-input multi-output systems. Real-world industrial applications were discussed across robotics, aerospace, power systems, renewable energy, avionics, targeting and tracking, and cyber-physical systems.

This FDP enriched Dr. Y.P. Patil's expertise in advanced control engineering and strengthened his ability to mentor students in nonlinear control, power electronics, robotics, and intelligent systems. His learnings contribute to the department's focus on modern control strategies relevant to emerging technologies such as autonomous systems, smart grids, IoT, and renewable energy integration.

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8 WEEK ONLINE NPTEL/AICTE FDP ON “RESEARCH METHODOLOGY”



PARTICIPATION OF MS. A.V. TIDKE IN THE PROGRAM ON “RESEARCH METHODOLOGY”

Ms.A.V. Tidke, Faculty of Electrical Engineering, successfully completed a 08-week online Faculty Development Program (FDP) on “Research Methodology” conducted through NPTEL–AICTE from July 2025 to September 2025.

This FDP was designed to enhance faculty understanding of the principles and implementation strategies of Research Methodology. The course provided comprehensive insights into Research and optimization techniques by completing this FDP, Ms.A.V. Tidke strengthened his understanding of the Research process, which will contribute to enhancing Research work, and overall academic quality within the department.

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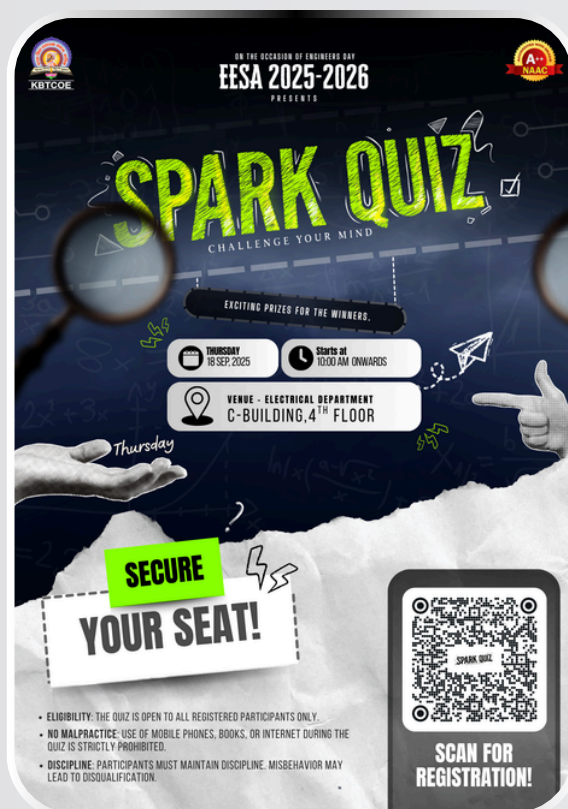
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– SPARK QUIZ – INSTITUTE LEVEL QUIZ COMPETITION

The Department of Electrical Engineering proudly organized the Spark Quiz Competition through the Electrical Engineering Students' Association (EESA) with enthusiastic participation from students. The quiz competition was designed as a three-round team event, evaluating students on technical awareness, general knowledge, and quick-thinking abilities.

The competition provided an engaging platform for students to demonstrate their knowledge, analytical thinking, and teamwork skills. Through this activity, students not only enhanced their technical awareness and problem-solving abilities but also developed confidence and communication skills while competing in a healthy and motivating environment.

The event successfully encouraged active student participation in extra-curricular activities, fostering a spirit of collaboration, intellectual curiosity, and healthy competition among participants. The department congratulated all the winning teams and participants for their enthusiastic involvement and commendable performance.



THE WINNING TEAMS OF THE QUIZ COMPETITION WERE FELICITATED IN THE PRESENCE OF HEAD OF DEPARTMENT DR. Y. P. PATIL, ALONG WITH EESA COORDINATORS PROF. P. V. JOSHI AND PROF. Y. S. BHAVSAR. THE ENTIRE FACULTY OF THE ELECTRICAL ENGINEERING DEPARTMENT ALSO GRACED THE OCCASION, MAKING THE EVENT MEMORABLE AND ENCOURAGING STUDENTS FOR FUTURE ACHIEVEMENTS

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EXPERT LECTURE ON CAD SOFTWARE & JOB OPPORTUNITIES

The Department of Electrical Engineering organized an expert lecture for Second Year B.Tech Electrical students on 04 February 2026. The session was delivered by Mr. Mayur Wagh from CAD Centre, Nashik. A total of 52 students attended the lecture.

The expert highlighted the importance of Computer Aided Design (CAD) tools in modern electrical engineering practices. Students were introduced to industry-oriented software such as AutoCAD Electrical, schematic design tools, panel layout design, wiring diagram preparation and electrical drafting techniques. The session also covered career opportunities in design engineering, project planning, industrial automation and electrical consulting.

The speaker explained how CAD Centre supports students through structured training, certification programs and hands-on practice, helping them become industry-ready professionals. The lecture was interactive and motivated students to develop software skills alongside their core engineering knowledge.



MR. MAYUR WAGH FROM CAD CENTRE, NASHIK EXPLAINING STUDENTS THE IMPORTANCE OF SOFTWARE RELATED TO ELECTRICAL ENGINEERING

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EXPERT LECTURE ON OPPORTUNITIES – ROBOTICS & AUTOMATION

The Department of Electrical Engineering organized an expert talk on “Opportunities – Robotics & Automation” on 13 February 2026 for Electrical Engineering students. The session was delivered by Mr. Sagar Patil, Founder & CEO of Raybot Automation Pvt. Ltd.

The program began with a welcome and felicitation of the guest. The speaker shared valuable insights into Robotics, Automation, Artificial Intelligence, and Machine Learning applications in modern industries. He explained the importance of integrating hardware knowledge with software skills and demonstrated practical AI model training techniques and real-world industrial use case.

Students gained awareness about career paths, internships, startup opportunities, and essential skills required for Industry 4.0. The session was highly interactive and inspired students to explore innovation-driven careers in robotics and automation.



**MR. SAGAR PATIL FOUNDER – CEO RAYBOT AUTOMATION, NASHIK EXPLAINING
STUDENTS THE IMPORTANCE AUTOMATION SKILLS**

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EXPERT LECTURE ON CAREER GUIDANCE AND ENTREPRENEURSHIP

An expert talk was delivered by Shri Sahebrao Popatrao Salunke, Director of Salunke Automation Pvt. Ltd., Nashik, who shared his inspiring journey from humble beginnings to becoming a successful entrepreneur in the field of electrical and automation engineering. During the session, he narrated his life story starting from his early days, his migration to Nashik, and his initial work experience in small-scale industries. He explained how continuous learning, practical exposure, and dedication helped him build expertise while working with reputed organizations like Siemens Ltd. After opting for voluntary retirement, he utilized his experience and skills to establish his own enterprise in 2006, which has now grown into Salunke Automation Pvt. Ltd.

He also provided insights into his company's operations, including automation solutions, electrical panels (LT & HT), AC/DC drives, PLC-based systems, and industrial control solutions. The company has successfully completed more than 1500 installations across India and abroad.

Shri Salunke emphasized the importance of skill development, practical knowledge, and perseverance for engineering students. He encouraged students to explore entrepreneurship opportunities and highlighted his contribution towards employment generation through skill training initiatives.

He also stressed the importance of physical fitness in personal and professional life, mentioning that swimming is a highly beneficial daily activity, and shared that he regularly practices it to maintain health, focus, and discipline. The session was successfully coordinated by Mr. R. B. Aware, Industry-Institute Coordinator, Department of Electrical Engineering, who maintained continuous interaction with Shri Salunke and facilitated the organization of this expert talk. The program was graced by the presence of Ms. Shrushti Shinde, Registrar, who felicitated Shri Salunke and appreciated his valuable contribution towards student development.

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The session was highly motivational and informative, providing students with real-world exposure to industrial practices and entrepreneurial thinking. The event was conducted under the guidance of Dr. Y. P. Patil, Head of the Department of Electrical Engineering. The department expresses sincere gratitude to Shri Sahebrao Salunke for his valuable time and inspiring interaction.

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PARENT-TEACHER MEET

A Parent-Teacher Meet (PTM) was conducted by the Department of Electrical Engineering on 14th March 2026 in Classroom C505. The purpose of the meet was to interact with parents and discuss students' academic performance, attendance, and overall progress.

Dr. Y. P. Patil, Head of the Department, addressed the parents and shared information about the institute's vision and mission. He also discussed student results and highlighted the importance of maintaining regular attendance for academic success.

Ms. R. V. Shinde conducted a counselling session on parenting. She guided parents on effective parenting practices and emphasized the importance of attendance and emotional support for students.

Academic achievers were felicitated during the event to recognize and encourage outstanding student performance.

The Department Technical Magazine was inaugurated by Mr. Vilas Deshmukh. The magazine showcases students' technical knowledge and achievements.

The event witnessed active participation from parents and faculty members. The session was interactive and beneficial, helping to strengthen the relationship between parents and the institute.

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STUDENT ACHIEVEMENT



Our department student Nikhil Zoman attained Five Day Skill Development Program on Electrical Vehicle Technology. The Department of Electrical Engineering (KKWIEER) conducted the Five-Day Skill Development Program on Electrical Vehicle Technology. In this program we learned the fundamentals of electric vehicle (EV) systems, including a clear understanding of different types of batteries and the role of the Battery Management System (BMS) in ensuring safety and efficiency. We also studied various types of motors used in EVs and how their speed is controlled. In addition, we gained knowledge of power electronics and its importance in EV operation. Along with theoretical concepts, we received hands-on exposure to EV components, which helped us in better understanding their real-world working and applications.

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STUDENT ACHIEVEMENT



Our department student Harshikesh Jankwar attained Five Day Skill Development Program on Electrical Vehicle Technology. The Department of Electrical Engineering (KKWIEER) conducted the Five-Day Skill Development Program on Electrical Vehicle Technology. In this program we learned the fundamentals of electric vehicle (EV) systems, including a clear understanding of different types of batteries and the role of the Battery Management System (BMS) in ensuring safety and efficiency. We also studied various types of motors used in EVs and how their speed is controlled. In addition, we gained knowledge of power electronics and its importance in EV operation. Along with theoretical concepts, we received hands-on exposure to EV components, which helped us in better understanding their real-world working and applications.

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IET - PRESENT AROUND THE NETWORK COLLEGE LEVEL PRESENTATION COMPETITION



The Department of Electrical Engineering successfully organized the “Present Around The Network” Competition on 14th March 2026 aimed at fostering technical awareness and presentation skills among students. This was the first round of district level PATN competition by IET.

A total of 25 students from various engineering programs and branches actively participated in the competition. The event provided a platform for students to present innovative ideas and concepts related to networking, communication systems, and modern technological advancements. Participants showcased their understanding through well-structured presentations, demonstrating their ability to analyze, interpret, and communicate technical information effectively. The competition encouraged students to think critically and present their ideas with clarity and confidence.

The event witnessed enthusiastic participation and healthy competition, promoting interdisciplinary interaction and knowledge sharing among students. Faculty members evaluated the presentations based on content quality, clarity, innovation, and delivery.

The winners of this competition will further represent the institute at the district-level competition, where participants from more than five engineering colleges across Nashik will compete. The district-level round is scheduled to be held on 11th April, providing students with an opportunity to compete at a larger platform and gain wider exposure.

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EXPERT TALK ON STRESS MANAGEMENT



An expert session on “Stress Management” was conducted by Dr. Manisha Kapadnis, a well-known dental doctor, counsellor, and a practitioner associated with the Brahma Kumaris. The session focused on understanding stress and its impact on students’ academic and personal lives.

Dr. Kapadnis elaborated on the concept of stress, its definition, and various types of stress. She explained modern-day stress issues faced by students and highlighted the connection between stress and psychosomatic diseases. The session provided valuable insights into how students can identify and manage day-to-day stress effectively. She also discussed practical remedies for stress management, including lifestyle modifications, positive thinking, and maintaining a balanced routine. A short-guided meditation exercise (3–5 minutes), inspired by Rajyoga meditation practices, was conducted, where students actively participated and experienced relaxation techniques.

The session was highly interactive and beneficial, helping students understand the importance of mental well-being and equipping them with simple yet effective strategies to handle stress in their daily lives.

The event was conducted under the guidance of Dr. P.D. Nemade, Head Civil Engineering and witnessed active participation from students and faculty members. The department expresses sincere gratitude to Dr. Manisha Kapadnis for her valuable guidance and motivating session.

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STUDENT ACHIEVEMENTS



The Innovation, Design and Entrepreneurship (IDE) Bootcamp is a prestigious national-level initiative of the Ministry of Education, Government of India, implemented through the All-India Council for Technical Education (AICTE) in collaboration with the Ministry of Education's Innovation Cell (MIC) and the Wadhvani Foundation. The program aims to nurture creativity, innovation, design thinking, and entrepreneurial skills among student innovators across the country. The IDE Bootcamp is designed as an intensive multi-day experiential learning program where students develop innovative solutions, refine their ideas, and present their projects before expert panels consisting of academicians, startup mentors, and industry professionals. This initiative plays a significant role in strengthening the innovation and startup ecosystem in alignment with the vision of the Government of India.

Karmaveer Adv. Baburao Ganpatrao Thakare College of Engineering (KBTCOE), Nashik, was privileged to be selected as the host institute for the Innovation, Design and Entrepreneurship (IDE) Bootcamp – Edition 3, Phase I, held from 06th April 2026 to 10th April 2026. The opportunity to host this prestigious national-level event reflects the institute's strong infrastructure, academic excellence, and commitment to fostering innovation, entrepreneurship, and multidisciplinary learning among students. Hosting the IDE Bootcamp provided a valuable platform for students and faculty members to interact with innovators, mentors, and participants from diverse institutions across India.

The IDE Bootcamp witnessed enthusiastic participation from numerous teams representing engineering colleges, polytechnics, and institutions across different regions of India.

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STUDENT ACHIEVEMENTS

Among the participating teams, Second Year B.Tech (Electrical Engineering) students of the Department of Electrical Engineering actively participated in the National Level IDE Bootcamp Pitching Competition. The team "Ash Tech Titans" presented an innovative project titled "Coal Ash Based Silicon Ion Battery," focusing on sustainable and eco-friendly battery technology utilizing coal ash-based materials. The project emphasized the utilization of industrial waste materials for developing advanced energy storage solutions, contributing to environmental sustainability and efficient resource utilization.

Competing against numerous teams from reputed institutions across India, the team demonstrated exceptional innovation, technical knowledge, teamwork, and presentation skills during the pitching sessions. Their innovative approach and practical implementation of ideas were well appreciated by the expert panel and mentors.

The team proudly secured 4th Position at the National Level IDE Bootcamp Pitching Competition, bringing recognition and pride to the Department of Electrical Engineering and the institute. The department congratulates the students for their outstanding achievement and encourages continued participation in innovation and entrepreneurship activities to further strengthen their technical and creative capabilities.

Name of Participants:

- 1. PARTH AHIRRAO**
- 2. YAMINI PATIL**
- 3. NIKHIL ZOMAN**
- 4. JAYESH BALWAD 5. PAYAL BODAKE**

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EXPERT TALK ON CYBER SECURITY AND CYBER LAWS



An expert talk on Cyber Security and Cyber Laws was conducted on 18th March 2026 at the C Building Seminar Hall as a Content Beyond Syllabus Activity for Second Year B.Tech students. The session was jointly organized by the Electrical Engineering and Electronics & Telecommunication Engineering departments to enhance students' awareness of cyber threats, digital safety, and legal frameworks associated with cybercrime.

The expert session was delivered by Dr. Nivedita Babulal Jadhav, Assistant Professor from MVP S's Law College, Nashik, who shared valuable insights into cybercrime laws, legal rights, and responsibilities associated with digital technologies. The session covered important topics such as cybercrime awareness, cyber laws under the IT Act 2000, legal remedies for cyber offenses, digital privacy protection, cyber fraud prevention, and safe use of social media platforms.

The talk significantly complemented the course Introduction to Cyber Security, particularly extending the legal and ethical aspects of cybercrime, cyber law provisions, and reporting mechanisms beyond the prescribed syllabus. The expert also discussed real-world cybercrime case examples, legal consequences of cyber offenses, and procedures for filing cybercrime complaints, which enhanced students' understanding of practical cyber law enforcement and digital rights. These advanced discussions strengthened students' knowledge related to cybercrime classification, IT Act provisions, and responsible use of digital platforms, which are core learning objectives of the cyber security course.

Students actively participated in the session by asking questions related to online frauds, digital payment security, social media misuse, cyberbullying, and privacy threats.

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IEEE YESIST12 2026 – PILOT ROUND

Proud Achievement!

Team MALVION from MVPS's KBTCOE, Nashik has achieved great success at IEEE YESIST12 2026 – Pilot Round. Their project "MargDarshak" stood out among 50+ teams across India.

The team secured 1st Prize (Innovation Challenge Track) and Overall 2nd Prize.

Special appreciation to Kritika Korimutha (SY Electrical) for her valuable contribution, making the Electrical Department proud.

The team has also qualified for the Grand Finale in Indonesia to represent the institute at the international level.



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VIRTUAL LAB HANDS-ON PRACTICAL SESSION ON CYBER SECURITY EXPERIMENTS



A hands-on Virtual Lab practical session was conducted on 18th April 2026 for Electrical Engineering students to provide practical exposure to Cyber Security concepts through simulation-based learning. The session aimed to enhance students' understanding of system security, vulnerabilities, and safe computing practices using standardized Virtual Lab procedures.

During the session, students actively performed five Virtual Lab experiments using their own mobile devices/laptops. The experiments were conducted step-by-step following the prescribed methodology available on the Virtual Lab platform.

The practical session included the following experiments:

- 1.Data Transformation in the MD5 Hashing Algorithm
- 2.Cryptographic Hash Function Properties
- 3.Exploring Buffer Overflow Vulnerabilities and Exploits
- 4.Format String Vulnerabilities
- 5.SQL Injection Vulnerabilities

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Through these experiments, students gained valuable insights into hashing mechanisms, memory vulnerabilities, and common web-based security threats. The visualization tools available in the Virtual Lab environment enabled students to understand internal processes such as data transformation, stack memory behaviour, and input validation techniques.

Students demonstrated the execution of each practical individually, while the instructor provided necessary guidance and technical support throughout the session. The activity promoted interactive learning, improved problem-solving skills, and strengthened students' awareness of cyber security practices.

The session was successfully conducted under the guidance of Dr. Y. P. Patil, Head of the Department of Electrical Engineering. The department appreciates the enthusiastic participation of students, which contributed to the successful completion of all planned practical experiments.

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INDUSTRIAL VISIT NASHIK WASTE MANAGEMENT PVT.LTD



"An industrial visit for S.Y. B.Tech students was organized under the subject of Environmental Studies at the Nashik Waste Management Authority. The primary objective of the visit was to bridge the gap between theoretical knowledge and practical implementation in the field of waste management and environmental sustainability.

During the visit, students were introduced to various stages of solid waste management, including collection, segregation, transportation, processing, and disposal. They observed modern techniques used for waste segregation at source, recycling of dry waste, and scientific treatment of organic waste. The officials at the facility also explained the importance of reducing landfill dependency and adopting eco-friendly waste management practices.

Students gained valuable insights into environmental protection techniques, the role of government bodies in maintaining urban cleanliness, and the significance of public participation in waste management systems. The interaction with industry experts helped students understand current challenges and innovative solutions in the field.

Overall, the visit was highly informative and enriched students' understanding of real-world environmental issues, encouraging them to adopt sustainable practices in their daily lives and future engineering careers."

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INDUSTRIAL VISIT WATER FILTRATION PLANT VIHOLI



"An industrial visit for S.Y. B.Tech students was organized under the subject of Environmental Studies at the Vilholi Water Filtration Plant. The primary objective of the visit was to provide students with practical exposure to water treatment and purification processes, bridging the gap between theoretical concepts and real-world applications.

During the visit, students observed various stages of water treatment, including screening, sedimentation, filtration, and disinfection. They learned about the techniques used to remove physical, chemical, and biological impurities to make water safe for drinking purposes. The plant officials explained the importance of maintaining water quality standards and the role of advanced treatment methods in ensuring a continuous supply of clean water.

Students gained valuable insights into water resource management, the significance of safe drinking water, and the challenges faced in water treatment and distribution systems. The interaction with technical staff helped them understand the operational aspects and maintenance of large-scale filtration system."