

INSTRUCTIONS FOR CANDIDATES

Candidates are requested to note the day, Date and Time of Paper.

- Candidates are required to be present at the Examination Centre, THIRTY MINUTES before the stipulated time.
- Candidates are forbidden from taking any material into the examination hall that can be treated as malpractice.
- Mobile phones are strictly NOT allowed in the examination hall.
- The candidate must carry his/her identity card and admit card for every examination.
- The candidates should not write anything on identity card, admit card and question paper.
- Candidates are requested to see the Notice Board at Examination Cell regularly for changes if any that maybe notified later in the schedule.
- The exchange of any material used in the examination hall is not permitted at the time of examination. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances.
- Use of non-programmable battery operated electronic pocket size Calculator is allowed. The exchange of calculators is not allowed. Any other electronic device except Calculator is prohibited in the examination hall.
- No request shall be granted for change in the time or date for the examination on any ground.
- The written examination will be conducted in the following order.

 Maratha Vidya Prasarak Samaj's Karmaveer Adv. Baburao Ganpatrao Thakare College of Engineering, Nashik (An Autonomous Institute Affiliated to Savitribai Phule Pune University, Pune)			Examination Time Table	
			ACADEMIC YEAR	2025-2026
MVP/KBTCE/2025-26/235 Exam Cell			Date: 23/04/2026	
EXAMINATION CELL			SEMESTER	I & II
FIRST YEAR M.TECH. ELECTRONICS & TELECOMMUNICATION ENGINEERING (VLSI AND EMBEDDED SYSTEM) 2024 PATTERN END SEMESTER EXAMINATION TIME TABLE MAY- JUNE 2026				
Course Code	Course Name	Day	Date	Time
SEMESTER - I				
204101	Digital CMOS Design	Monday	18/05/2026	10:00 am to 12:30 pm
204102	Digital System Design	Wednesday	20/05/2026	10:00 am to 12:30 pm
204103	Embedded System Design	Friday	22/05/2026	10:00 am to 12:30 pm
201104	Research Methodology & IPR@	Monday	25/05/2026	10:00 am to 12:30 pm
204105C	Elective-I: Embedded Automotive System	Thursday	28/05/2026	10:00 am to 12:30 pm
SEMESTER - II				
204201	Analog CMOS Design	Tuesday	19/05/2026	10:00 am to 12:30 pm
204202	Devices Modeling for VLSI	Thursday	21/05/2026	10:00 am to 12:30 pm
204203	Real Time Operating System	Saturday	23/05/2026	10:00 am to 12:30 pm
204204C	Elective-II: Embedded System for Biomedical	Tuesday	26/05/2026	10:00 am to 12:30 pm
200205	Introduction to Cyber security @	Friday	29/05/2026	10:00 am to 12:30 pm

@ Common to all branches


Dr. S. P. Mogal
 COE




Dr. S. R. Devane
 Director