




20th August 2024

NOTICE

All BOS members are hereby requested to attend the first BOS meeting schedules on **Friday, 30th August 2024 at 2.00pm.**

Agenda for the Board of Studies (BOS) Meeting

1. Welcome Address
 - Opening remarks and welcome by the Chairperson.
2. Introduction of BOS Members
 - Brief introduction of all attending BOS members online/offline.
3. Objective of the Meeting
 - Discussion of meeting objectives, including the review of the Basic Electronics syllabus for first-year students for AY 2024-25.
4. Overview of the Department
 - A presentation on the current status, achievements, and vision of the Department of Electronics and Telecommunication Engineering.
5. New Syllabus - NEP Framework
 - Introduction and alignment of the new syllabus with the National Education Policy (NEP) guidelines.
6. Curriculum Structure
 - Review and discussion of the overall structure of the syllabus, including credit distribution and assessment methods.
7. Electronics Subject Content
 - Detailed discussion on the proposed content of the Basic Electronics subject across all relevant branches.
8. Open Discussion
 - An open floor for suggestions, feedback, and further discussion from BOS members.
9. Vote of Thanks
 - Closing remarks and expression of gratitude to all participants.


Dr. V. M. Birari
Chairman BOS



Department of Electronics & Telecommunication Engineering

Day & Date: Friday, 30th August 2024

Time: 2.00 pm on wards

Venue: C-408, Department of E & TC, 3rd Floor, Academic Building C, KBTCOE, Near Pumping Station, Gangapur Road, Nashik -13

Agenda of the meeting-

- ◆ Welcome Address
- ◆ Introduction
- ◆ Objectives of the meeting
- ◆ About the Department
- ◆ New Syllabus NEP Based
- ◆ Structure
- ◆ Electronics Subject Contents:-
 - a. In-depth review of the syllabus content related to Electronics.
 - b. Feedback and suggestions from the BOS members.
- ◆ Open Discussion
- ◆ Vote of Thanks

| SN | Name | Organization | BOS Position | Sign |
|----|---------------------|--|----------------------------|-------------|
| 1 | Dr. S. R. Devane | KBT CoE, Nashik | Director | |
| 2 | Dr. Vijay M. Birari | KBT CoE, Nashik | Chairman BOS | |
| 3 | Dr.M.H.Kori | Retd. Technical Director, ALCATEL-LUCENT&Technology Advisor to Validus Technologies, USA | Subject Expert | online |
| 4 | Dr.S.L.Nalbalwar | Professor & Head,Department of Electronics & Telecomm. Engg. BATU, Lonere Tal.:Mangaon Dist.: Raigad, M.S., India | Subject Expert | online |
| 5 | Mr. Amol Deshmukh | Head R&D Rishabh Instruments Ltd. F-31, MIDC Satpur Nashik - 422007 Maharashtra India | Industry Representative | 30/8/24 |
| 6 | Dr.Munir Sayyad | General Manager Reliance Corporate Park Thane Belapur Road Ghansoli, Ghansoli, Maharashtra, 400701, India | Industry Representative | online |
| 7 | Ms.Komal Pise | Director, Sri Sai precision Instruments, Satpur Nashik. | Industry Representative | |
| 8 | Dr. Vijay M. Birari | KBT CoE, Nashik | Member | |

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|----|-------------------------|-----------------|----------------------|----------------------------|
| 9 | Mr. B. N. Shinde | KBT CoE, Nashik | Member | <u>B.N. shinde</u> |
| 10 | Dr. U. V. Patil | KBT CoE, Nashik | Member | <u>U. V. Patil</u> |
| 11 | Mr. A. P. Meshram | KBT CoE, Nashik | Member | <u>A. P. Meshram</u> |
| 12 | Mr. B. J. Pawar | KBT CoE, Nashik | Member | <u>B. J. Pawar</u> |
| 13 | Ms. S. A. Dhumane | KBT CoE, Nashik | Member | <u>S. A. Dhumane</u> |
| 14 | Mr. A. R. Chaudhari | KBT CoE, Nashik | Member | <u>A. R. Chaudhari</u> |
| 15 | Dr. S. M. Jagtap | KBT CoE, Nashik | Member | <u>S. M. Jagtap</u> |
| 16 | Mr. B. N. Rajole | KBT CoE, Nashik | Member | <u>B. N. Rajole</u> |
| 17 | Ms. D. V. Patil | KBT CoE, Nashik | Member | <u>D. V. Patil</u> |
| 18 | Ms. T. S. Deshmukh | KBT CoE, Nashik | Member | <u>T. S. Deshmukh</u> |
| 19 | Ms. S. M. Dharrao | KBT CoE, Nashik | Member | <u>S. M. Dharrao</u> |
| 20 | Mr. V. R. Sonawane | KBT CoE, Nashik | Member | <u>V. R. Sonawane</u> |
| 21 | Ms. J. S. Patil | KBT CoE, Nashik | Faculty-Non Teaching | <u>J. S. Patil</u> |
| 22 | Ms. S. S. Gade | KBT CoE, Nashik | Faculty-Non Teaching | <u>S. S. Gade</u> |
| 23 | Mr. V. B. Thakare | KBT CoE, Nashik | Faculty-Non Teaching | - |
| 24 | Ms. Shital S. Mogal | KBT CoE, Nashik | Faculty-Non Teaching | <u>Shital S. Mogal</u> |
| 25 | Mr. Dattatray R. Handge | KBT CoE, Nashik | Faculty-Non Teaching | <u>Dattatray R. Handge</u> |
| 26 | Mr. Akshay Jadhav | KBT CoE, Nashik | Faculty-Non Teaching | - |

Minutes of Meeting

| Sr. No. | Points Discussed | Action/Decision | Responsibility | Target Date |
|---------|--------------------------------|---|---|-------------|
| 1. | Welcome Address & Introduction | Dr. V. M. Birari, Chairman BOS welcomed all the BOS members, and Dr. U. V. Patil introduced all the BOS members present at the meeting (in hybrid mode). | Dr. V. M. Birari Dr.U.V.Patil | - |
| 2. | 1. Objective of the Meeting | 2. To review and suggest improvements to the new syllabus based on the National Education Policy (NEP) guidelines. 3. To approve the structure of the new syllabus for the Basic Electronics Engineering subject, which is common to multiple branches (Electrical, Mechanical, Instrumentation, Computer, IT, AI, and DS). 4. To ensure the syllabus content is aligned with the latest industry trends and meets the requirements of various engineering disciplines. 5. To discuss the inclusion of modern technologies and pedagogical approaches in the curriculum. 6. To open the floor for suggestions and discussions from the BOS members. | All BOS members | - |
| 3. | About the Department | Chairman of BOS and Head of the E&TC Engineering Department, provided detailed information about the department to all the BOS members | Dr. V. M. Birari | - |
| 4. | New Syllabus NEP Based | The new curriculum is designed in accordance with NEP-2020 | - | - |
| 5. | Structure | The structure will be designed according to the guidelines provided by NEP-2020 and the GR of the State Government of Maharashtra. The structure will be presented to all BOS members. | Dr.U.V.Patil | - |
| 6. | Electronics Subject Contents | The content of Basic Electronics was presented by Mr. B. J. Pawar to the BOS members. The members suggested a few changes to the syllabus. The BOS Chairman asked the other members to include the suggested topics. | Mr.B.J.Pawar Mr.R.N.Rajole Ms.Shital Dharro | 31/8/2024 |
| 7. | Open Discussion | To discuss and implement changes in the Basic Electronics Engineering syllabus, which is common to all first-year branches, including Computer, IT, AI & DS, Civil, Mechanical, Electrical, Instrumentation & Control, and Electronics & Telecommunication (E&TC) Engineering. Key Discussions and Suggestions: 1. Dr. M. H. Kori's Suggestions: a. Semiconductor Materials: | All members | |

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| | <p>Dr. Kori emphasized the need for students to familiarize themselves with semiconductor materials beyond Silicon (Si) and Germanium (Ge).</p> <p>Action: It was agreed to introduce other semiconductor materials (e.g., Gallium Nitride, Silicon Carbide) in the prerequisites section of the syllabus.</p> <p>b. Unit 5 – Communication Topic: Dr. Kori recommended removing the communication topic, stating that it is not relevant to the current syllabus. Instead, he proposed introducing the Thyristor family in Unit 5.</p> <p>Action: The communication topic has been removed, and the Internet of Things (IoT) has been incorporated to align with industry trends. The Thyristor family has been moved to a more suitable part of the syllabus.</p> <p>2. Dr. Munir Sayyad’s Suggestions:</p> <p>a. Bloom’s Taxonomy: Dr. Sayyad suggested applying Bloom’s Taxonomy in the while designing the syllabus to ensure the cognitive development of students with suitable example of each BT level.</p> <p>Action: The syllabus has been restructured with clear learning outcomes based on Bloom’s Taxonomy.</p> <p>b. Project-Based Learning (PBL): Dr. Sayyad recommended integrating project-based learning to enhance the practical skills of students.</p> <p>Action: A focus on project-based learning has been added to the syllabus to ensure hands-on experience and real-world application of theoretical knowledge.</p> <p>c. Industry Alignment: He highlighted the importance of aligning the syllabus with current industry needs to develop relevant skill sets.</p> <p>Action: The syllabus now incorporates industry-relevant content and practices to better equip students for the demands of the modern workforce.</p> <p>d. Integration of MOOCs:</p> | | |
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| | <p>Dr. Sayyad advised integrating MOOCs or online content to provide supplementary learning opportunities.</p> <p>Action: Students will be encouraged to complete relevant MOOC courses as part of their continuous learning and skill development.</p> <p>3. Mr. Amol Deshmukh's Suggestions:</p> <p>a. Introduction of IoT and Thyristor Family: Mr. Deshmukh recommended the inclusion of IoT and Thyristor family to provide exposure to current trends in industry.</p> <p>Action: IoT has been added to Unit 5, while the Thyristor family will be discussed in the second-year syllabus.</p> <p>b. Microprocessor and Microcontroller: Instead of focusing on the 8085 Microprocessor, Mr. Deshmukh suggested introducing a comparative study of Microprocessors and Microcontrollers.</p> <p>Action: The syllabus now includes an introduction and comparison of Microprocessors and Microcontrollers, offering a more holistic understanding of embedded systems.</p> <p>c. Variable Voltage Regulator: He proposed replacing the fixed regulator with the variable voltage regulator for better understanding of power electronics.</p> <p>Action: The variable regulator will be introduced in the second-year syllabus as part of an advanced study.</p> <p>4. Ms. Komal Pise's Suggestions:</p> <p>a. Emerging Technologies (Bluetooth, 5G, AI, ML): Ms. Pise recommended including emerging technologies such as Bluetooth, 5G, Artificial Intelligence (AI), and Machine Learning (ML) in the syllabus.</p> <p>Action: These topics will be covered in an external workshop to provide students with insights into the latest technologies and their applications.</p> <p>b. Calibration:</p> | | |
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| | | <p>Ms. Pise inquired about the need for including calibration in the syllabus.</p> <p>Action:</p> <p>After discussion, it was decided that calibration will be covered in specific workshops or practical sessions to ensure relevance.</p> | | |
| 8. | Conclusions: | <p>The Board agreed to implement the proposed changes to ensure the syllabus remains comprehensive, up-to-date, and aligned with both academic and industry requirements. The updates reflect the evolving needs of various engineering branches while ensuring students are equipped with foundational knowledge in Basic Electronics .</p> <p>The meeting concluded with the decision to finalize and circulate the updated syllabus for implementation in AY 2024-25.</p> <p>Meeting Adjourned. Recorded by: Ms. T.S. Deshmukh Approved by: Dr. V. M. Birari, Chairman, BOS</p> <p>This revised version provides a structured and professional approach to documenting the discussions and actions of the BOS meeting.</p> | | |
| 9 | Vote of Thanks | <p>Mr. Bhushankumar Shinde proposed the vote of thanks to all BOS members and assured them we will make necessary changes as per the suggestions.</p> | | |



Dr. V. M. Birari
Chairman BOS
Head of Department E&TC Engg.

Enclosure:

1. First draft of Syllabus
2. Updated Draft of Syllabus

Chairman
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