



MARATHA VIDYA PRASARAK SAMAJ'S
KARMAVEER ADV. BABURAO GANPATRAO THAKARE
COLLEGE OF ENGINEERING



Permanently Affiliated to Savitribai Phule Pune University Vide Letter No: CA/1542
& Approved by AICTE New Delhi Vide Letter No: 740-89-32 (E) ET/98
AISHE Code - C-41622

www.kbtcoe.org

AICTE - Prerana Scheme

Schedule for GATE Computer Classes

Academic year 2021-22

Time of session: 3 to 5 p.m.

Class location: Class room no. 201, Building 'B', Mechanical Engineering Dept.

Mode of session conduction: Offline

Lecture No.	Date	Day	Topic	Name of faculty
1	26 th Nov 2021	Friday	Digital Logic	Mr. B.S.Tarle
2	27 th Nov 2021	Saturday	Computer Organization and Architecture	Mr. P.P.Shinde
3	3 rd Dec 2021	Friday	Programming and Data Structures	Ms. R.P.Chandwadkar
4	4 th Dec 2021	Saturday	Algorithms	Ms. D.R.Gatne
5	10 th Dec 2021	Friday	Theory of Computation	Mr. V.S.Tidake
6	11 th Dec 2021	Saturday	Compiler Design	Ms. P.P.Boraste
7	17 th Dec 2021	Friday	Operating System	Ms. R. R. Tajanpure
8	18 th Dec 2021	Saturday	Databases	Ms. B.A.Ahire
9	23 rd Dec 2021	Friday	Computer Networks	Mr. S.A.Talekar
10	24 th Dec 2021	Saturday	Probability and Statistics	Ms. V.A.Tamboli
11	31 st Dec 2021	Friday	Calculus	Ms. J.J.Nerkar
12	1 st Jan 2022	Saturday	Linear Algebra	Ms. J.V.Pagar
13	7 th Jan 2022	Friday	Discrete Mathematics	Ms. M.B.Thombare

Dr. S.B.Sonawane
Coordinator

Dr. V.S.Tidake/Dr. V.R.Sonawane
HOD (Computer Engg.)/HOD (IT)

Dr. S.R.Devane
Principal