



MARATHA VIDYA PRASARAK SAMAJ'S
KARMAVEER ADV. BABURAO GANPATRAO THAKARE
COLLEGE OF ENGINEERING



Permanently Affiliated to Savitribai Phule Pune University Vide Letter No: CA/1542
& Approved by AICTE New Delhi Vide Letter No: 740-89-32 (E) ET/98
AISHE Code - C-41622

www.kbtcoe.org

Two Days Faculty Development Program

on

Microprocessor and

Microprocessor Laboratory

(01st and 02nd February, 2021)

Organized by

Department of Computer Engineering,

MVPS's KBT COE, Nashik

In association with



BoS Computer Engineering,

Savitribai Phule Pune University, Pune

Schedule 01st February 2021

Date/Day	Time	Activities/ Session
01/02/2021 (Monday)	10.15 AM to 10.30 AM	Welcome / Inaugural Speech
	10.30 AM to 10.35 AM	Introduction of BoS Invitees and Speakers
	10.35 AM to 10.45 AM	Address by Chief Guest
	10.45 AM to 12.15 PM	Session 1: Prof. Dr. Nilesh Sable, JSPM's Imperial College of Engineering and Research, Wagholi, Pune
	12.15 PM to 01.45 PM	Session 2: Prof. Mahendra Salunke, Pimpri Chinchwad College of Engineering and Research, Ravet
	01.45 PM to 02.15 PM	Lunch Break
	02.15 PM to 03.45 PM	Session 3: Prof. Swati Joshi, Sinhgad College of Engineering, Pune

Schedule 02nd February 2021

Date/Day	Time	Activities/ Session
02/02/2021 (Tuesday)	10.30 AM to 12.00 Noon	Session 1: Prof. Malan Sale, Sinhgad College of Engineering, Pune
	12.00 Noon to 01.15 PM	Session 2: Prof. Dr. Nilesh Sable, JSPM's Imperial College of Engineering and Research, Wagholi, Pune
	01.15 PM to 01.45 PM	Lunch Break
	01.45 PM to 03.00 PM	Session 3: Prof. Mahendra Salunke, Pimpri Chinchwad College of Engineering and Research, Ravet
	03.00 PM to 04.30 PM	Session 4: Prof. Swati Joshi, Sinhgad College of Engineering, Pune
	04.30 PM to 04.45 PM	Conclusion and Vote of thanks by Mrs. V. S. Tidake, FDP Coordinator