

Faculty Development Programme on “Microprocessor and Microprocessor Laboratory”



Maratha Vidya Prasarak Samaj's
Karmaveer Adv. Baburao Ganpatrao
Thakare College of Engineering, Nashik
Accredited by NAAC with an 'A' Grade

Department of Computer Engineering

Organizes

Two Days

Faculty Development Programme

On

SE Computer Revised Syllabus (2019 Course) Sem -II

For the Subject

“Microprocessor and Microprocessor
Laboratory”

Date: 1st & 2nd February, 2021

In Association with



Board of Studies - Computer Engineering,
SPPU, Pune

ABOUT THE INSTITUTE

MVP Samaj's Karmaveer Adv. Baburao Ganpatrao Thakare College of Engineering was established in 1999 having 11 acres area in the heart of the Nashik city at approximately 2 kms from CBS and 12 kms from Nashik Road Railway station. The college is Permanently Affiliated to Savitribai Phule Pune University, Pune and Approved by AICTE, New Delhi. Our institute is NAAC Accredited institute with 'A' Grade. The college is developing fast with the vision “To be internationally accredited, Multidisciplinary, and Multi-collaborative institute working on technology enabled platform fostering innovations and patents through state-of-art academic system designed by highly qualified faculty for the development of common masses at large” and the mission “To educate and train common masses through undergraduate, post graduate, research programs by inculcating the values for discipline, quality, transparency and foster career and professional development for employment thereby contributing to the development of society”.

ABOUT THE DEPARTMENT

Department of Computer Engineering was established in 1999. The Vision of the Department is to be the center for excellence for training the world-class engineers to work with multidisciplinary domain based on the state-of-the-art of technology enabled academic system blended with industrial and business practices. Department has fully equipped labs with 100 Mbps leased line. All labs have latest powerful systems and blade servers, workstations, printers, scanners, etc. connected with Wi-Fi. All labs in the department are well-equipped with required hardware and software for developing customized applications in the field of Database Management, Networking, IoT, Data Science, Artificial Intelligence, Machine Learning and Cyber Security.

OBJECTIVES OF THE FDP

The aim of this Faculty Development Program (FDP) is to enhance the theory as well as practical knowledge skill of the teachers by providing the in sight of the subjects Microprocessor and Microprocessor Laboratory.

Registration Details: Free

Link for Registration:

<https://forms.gle/2wwck1ZhhKbNwKdH9>

Join WhatsApp Group after Registration:

<https://chat.whatsapp.com/GG5c1B4N1cA9H0YkxzVwLh>

For queries contact: Mrs. V. S. Tidake - 9422751559

CHIEF PATRONS

Hon. Dr. Tushar R. Shewale, President, MVP Samaj, Nashik
Hon. Smt. Neelimatai V. Pawar, Sarchitnis, MVP Samaj, Nashik
Hon. Shri. Manikrao M. Boraste, Sabhapati, MVP Samaj, Nashik
Hon. Shri. Ragho K. Ahire, Upsabhapati, MVP Samaj, Nashik
Hon. Dr. Sunil U. Dhikale, Chitnis, MVP Samaj, Nashik

PATRONS

Dr. N. S. Patil, Education Officer, MVP Samaj, Nashik
Dr. S. R. Devane, Principal, MVPS's KBT COE, Nashik
Prof. N. B. Desale, Vice Principal, MVPS's KBT COE, Nashik

CONVENER

Dr. V. S. Pawar, Head, Computer Engineering

ADVISORY COMMITTEE

Dr. Varsha Patil (Chairman, BoS-Computer, SPPU, Pune)
Dr. Aditya Abhyankar (Chairman, BoS-IT, SPPU, Pune)
Dr. Shirish S. Sane (Member, BoS- Computer, SPPU, Pune)
Dr. Madan U. Kharat (Member, BoS-IT, SPPU, Pune)
Dr. Parikshit N. Mahalle (Member, BoS-Computer, SPPU, Pune)
Dr. Pramod D. Patil (Member, BoS-Computer, SPPU, Pune)
Dr. Prashant M. Yawalkar (Member, BoS-Computer, SPPU, Pune)

COORDINATOR

Mrs. V. S. Tidake, Associate Professor, Computer Engineering

ORGANIZING COMMITTEE

Mr. B. S. Tarle	Mr. S. A. Talekar
Mr. P. P. Shinde	Ms. R. R. Shevale
Ms. R. P. Chandwadkar	Mr. S. T. Datir
Ms. P. V. Shinde	Ms. S. V. Pawar
Ms. M. B. Thombare	Ms. P. P. Boraste
Mr. B. S. Gholap	Ms. M.V. Malode
Ms. D. L. Tidke	

